

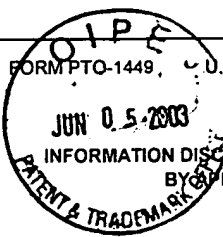
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EG	4	WO 96/09583 A	9/25/95	PCT
*	5	JP 06 332683 A	12/2/94	Japan
*	6	JP 2001-282648 A	10/12/01	Japan
EG	7	PCT/GB 95/02283	01/22/96	PCT - Int

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	

* NOT CONSIDERED.
NOT ENGLISH LANGUAGE, E.G.

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FORM PTO-1449, U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.
ARC.017A

APPLICATION NO.
10/083,057

IDS # 7

APPLICANT
Daniel Hansson

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FILING DATE
October 25, 2001

GROUP
2123

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
EG	1	4,216,539	08/05/80	Raymond, et al.			
EG	2	5,294,928	03/15/94	Cooper, et al.			
EG	3	5,333,119	07/26/94	Raatz, et al.			
EG	4	5,566,357	10/15/96	Holcman			
EG	5	5,713,028	01/27/98	Takahashi, et al.			
EG	6	5,768,602	06/16/98	Dhuey			
EG	7	5,826,079	10/20/98	Boland, et al.			
EG	8	5,878,264	03/02/99	Ebrahim			
EG	9	5,893,151	04/06/99	Merchant			
EG	10	5,931,950	08/03/99	Hsu			
EG	11	5,950,120	09/07/99	Gardner, et al.			
EG	12	6,021,500	02/01/00	Wang, et al.			
EG	13	6,035,374	03/07/00	Panwar, et al.			
EG	14	6,058,142	05/02/00	Ishikawa, et al.			
EG	15	6,128,747	10/03/00	Thoulon			
EG	16	6,163,851	12/19/00	Yamazoe, et al.			
EG	17	6,240,521	05/29/01	Barber, et al.			
EG	18	6,308,249	10/23/01	Okazawa			
EG	19	6,308,279	10/23/01	Toll, et al.			
EG	20	6,571,333	05/27/03	Jain, et al.			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

EXAMINER

EG GARCIA-OTERO

DATE CONSIDERED

5/12/04

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. ARC.017A	APPLICATION NO. 10/083,057
	APPLICANT Daniel Hansson		
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EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
EG	21	Chalmers University of Technology, Department of Computer Engineering (1999) Evaluation of Techniques for Reducing the Energy-Delay Product in a JAVA Processor by Daniel Back, Anders Loof, Martin Ronnback and Daniel Sandstrom (12 pages).
EG	22	Low Power Architecture Design and Compilation Techniques for High-Performance Processors Article by Ching-Long Su, Chi-Ying Tsui, and Alvin M. Despain (2/15/94) published in Proceedings of COMPCON 1994, April 1994.
EXAMINER <i>EDG, ARCIA - OTC</i>		DATE CONSIDERED <i>5/12/04</i>
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